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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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PATENT

INVENTOR: John Bruley et al.

) EXAMINER: Phuc T. Dang

SERIAL NO.: 10/605,258

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FILING DATE: September 18, 2003

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FOR: SITE-SPECIFIC METHODOLOGY  
FOR LOCALIZATION AND  
ANALYZING JUNCTION DEFECTS  
IN MOSFET DEVICES)

**CHANGE OF CORRESPONDENCE ADDRESS FOR ALL PURPOSES**

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Dear Sir:

Pursuant to 37 CFR §1.33, please change the correspondence address, (Customer No. 32074), for all purposes for the above identified patent application and resulting patent to:

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Respectfully submitted,

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